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4 Number of Pages (including this page)

Date: March 8, 2005

To: MS: AFTER FINAL

Location: United States Patent and Trademark Office

Fax No.: (703) 872-9306

From: Robert L. King (Registration No. 30,185)

Subject: S/N 10/074,732 - Leo Mathew et al.

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MESSAGE:

Enclosed herewith is a Notice of Appeal for the above-referenced patent application.

ALL ITEMS MARKED WITH AN "X" ARE INCLUDED IN THE FAX

1.	x	1 page Fax cover sheet
2.	x	1 page Fee Transmittal (in duplicate)
3.	X	1 page Notice of Appeal

PAGE 2/4 * RCVD AT 3/8/2005 11:29:09 AM [Eastern Standard Time] * SVR:USPTO-EFXRF-1/1 * DNIS:8729306 * CSID:5129966853 * DURATION (mm-ss):01-46

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DOCKET NO. SC11805TP

UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT Leo Mathew et al. GROUP ART UNIT: 2815
APPLN. NO.: 10/074,732 EXAMINER: Paul E Brock II
FILED: February 13, 2002 CONFIRMATION No.: 6470
TITLE: METHOD OF FORMING A VERTICAL DOUBLE GATE
SEMICONDUCTOR DEVICE

Certificate of Transmission under 37 C.F.R. 1.8

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**NOTICE OF APPEAL FROM THE PRIMARY EXAMINER TO THE
BOARD OF APPEALS**

HONORABLE COMMISSIONER FOR PATENTS

P.O. Box 1450

Alexandria, VA 22313-1450

SIR:

Applicants hereby appeal to the Board of Appeals from the decision dated December 10, 2004, of the Patent Examiner, Paul E Brock II, finally rejecting claims 1, 2, 4, 5, 7-12, 14-21, 24, 25 and 34 and withdrawing claims 35 and 35 from consideration. The \$500.00 Notice of Appeal Fee is requested to be charged to Deposit Account No. 503079 (Freescale Semiconductor, Inc.). A duplicate copy of this Notice is enclosed herewith.

Respectfully submitted,
LEO MATHEW et al.

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